501.39288CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

SATO et al

Serial No.:

Rule 1.53(b) continuation of U.S. Patent Application Serial

No. 09/709,403 filed November 13, 2000

Filed:

Herewith

For:

METHOD OF MANUFACTURING A SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE AND A SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Group of Parent:

2813

Examiner of Parent: W. Vesperman

<u>UNDER 37 CFR §1.97 and § 1.98</u>

Mail Stop DD Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450 November 26, 2003

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List of documents cited in prior application Serial No. 09/709,403, filed November 13, 2000.

Since application Serial No. 09/709,403 is being cited under 35 USC §120 in the above-identified continuation application, copies of the listed documents are not enclosed. See 37 CFR §1.98(d).

To the extent that the above-listed documents are not English, the requirements of 37 CFR §1.98(a)(3) are satisfied at least by the English translations enclosed with the documents as submitted in prior application Serial No. 09/709,403.

This Information Disclosure Statement is being submitted concurrently with the filing of the above-identified Continuation application.

In view of all of the foregoing, consideration of the listed documents, upon examination of the above-identified application, is respectfully requested.

Kindly charge any additional fees due, or credit overpayment of fees, to Deposit Account No. 01-2135 (501.39288CX1).

Respectfully submitted,

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Sheet 1 of 2

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